

Figure 1

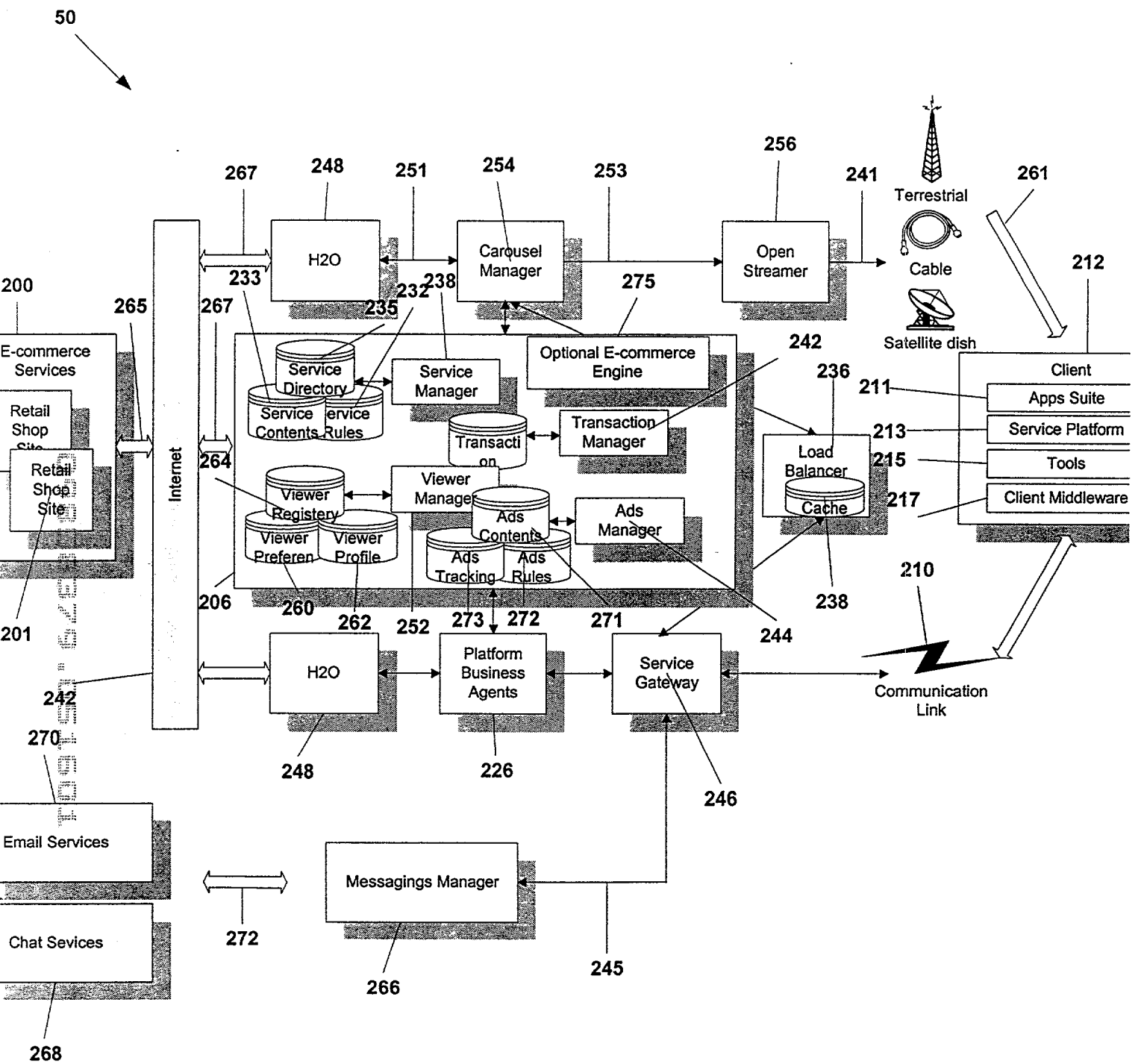


Figure 2

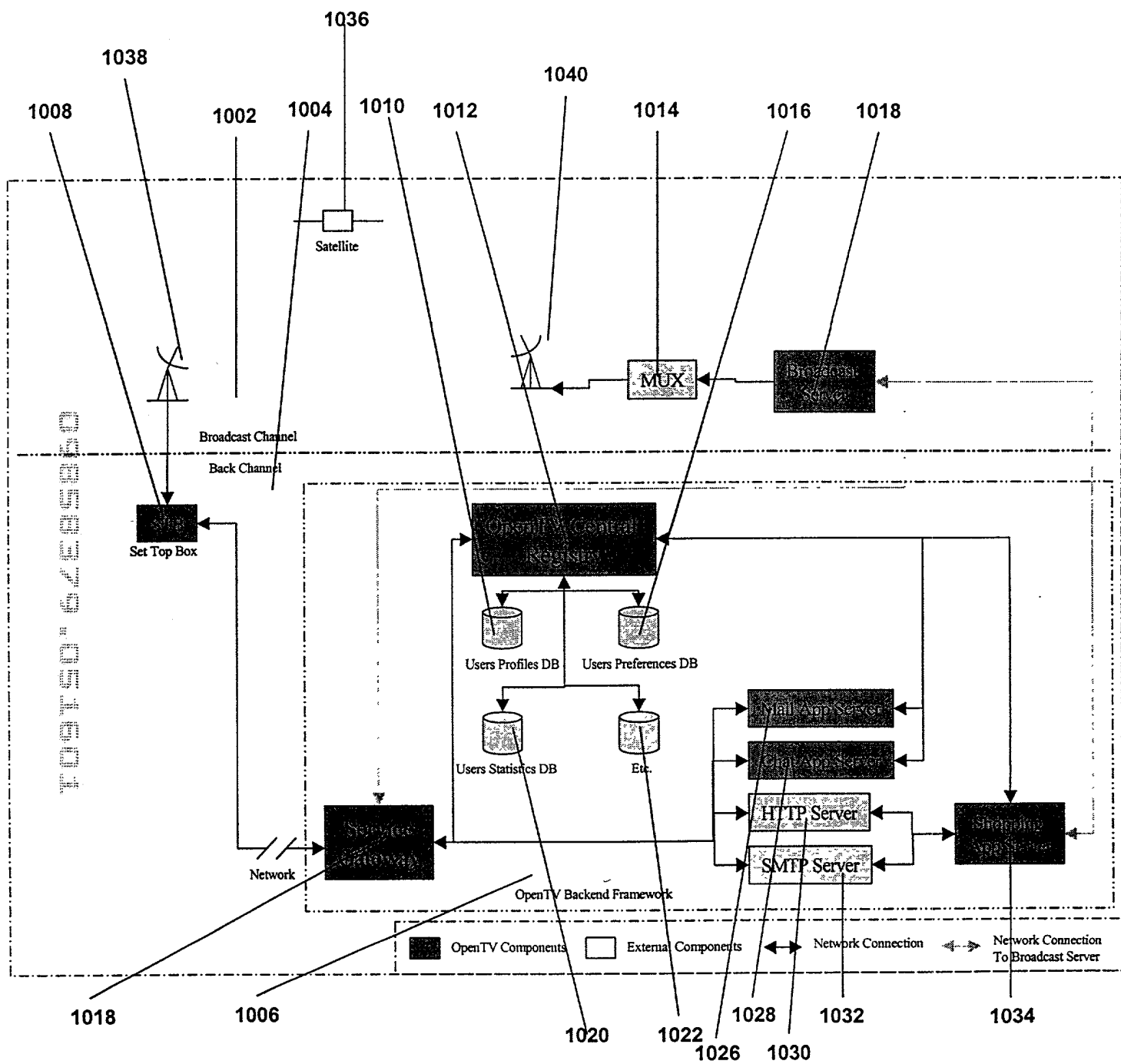


Figure 3

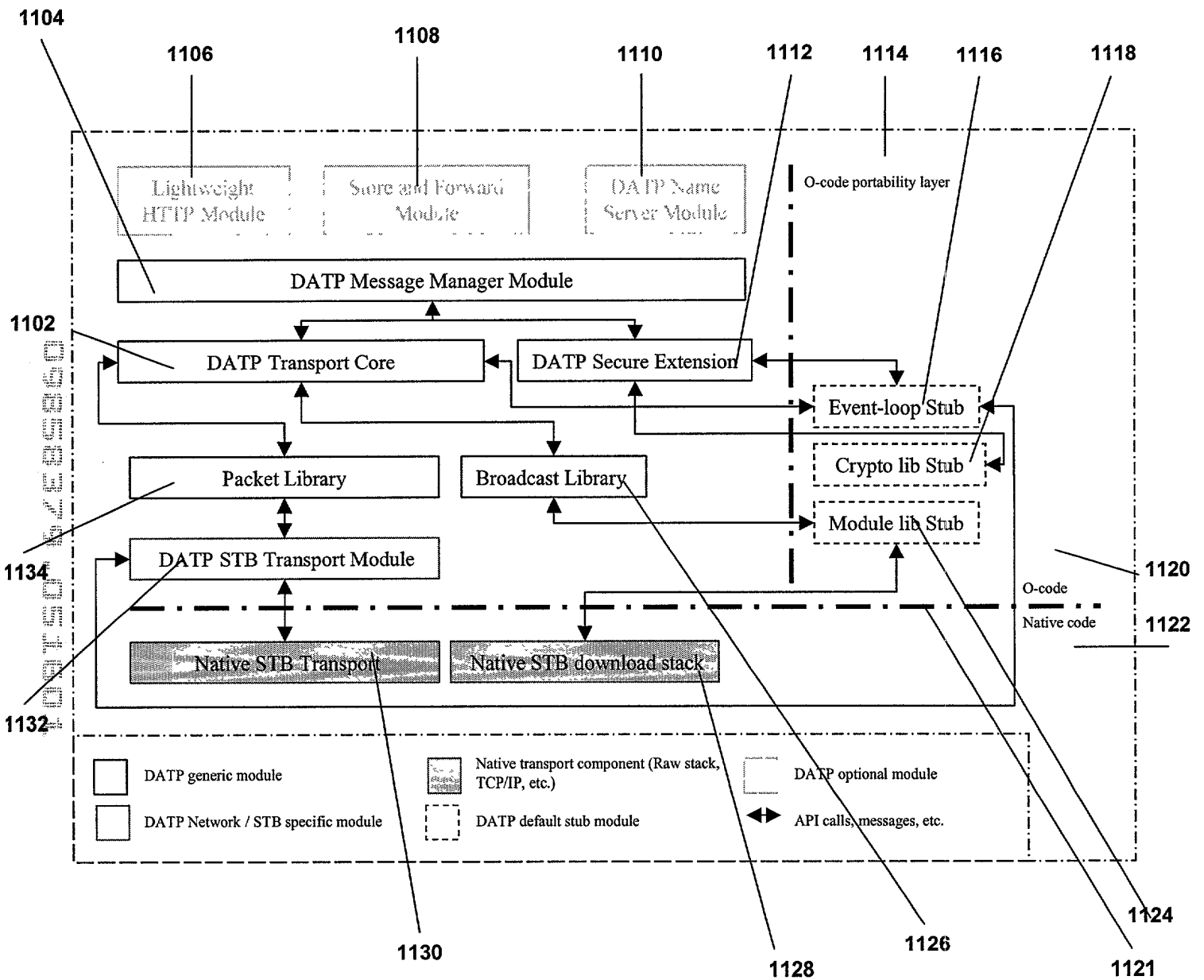


Figure 4

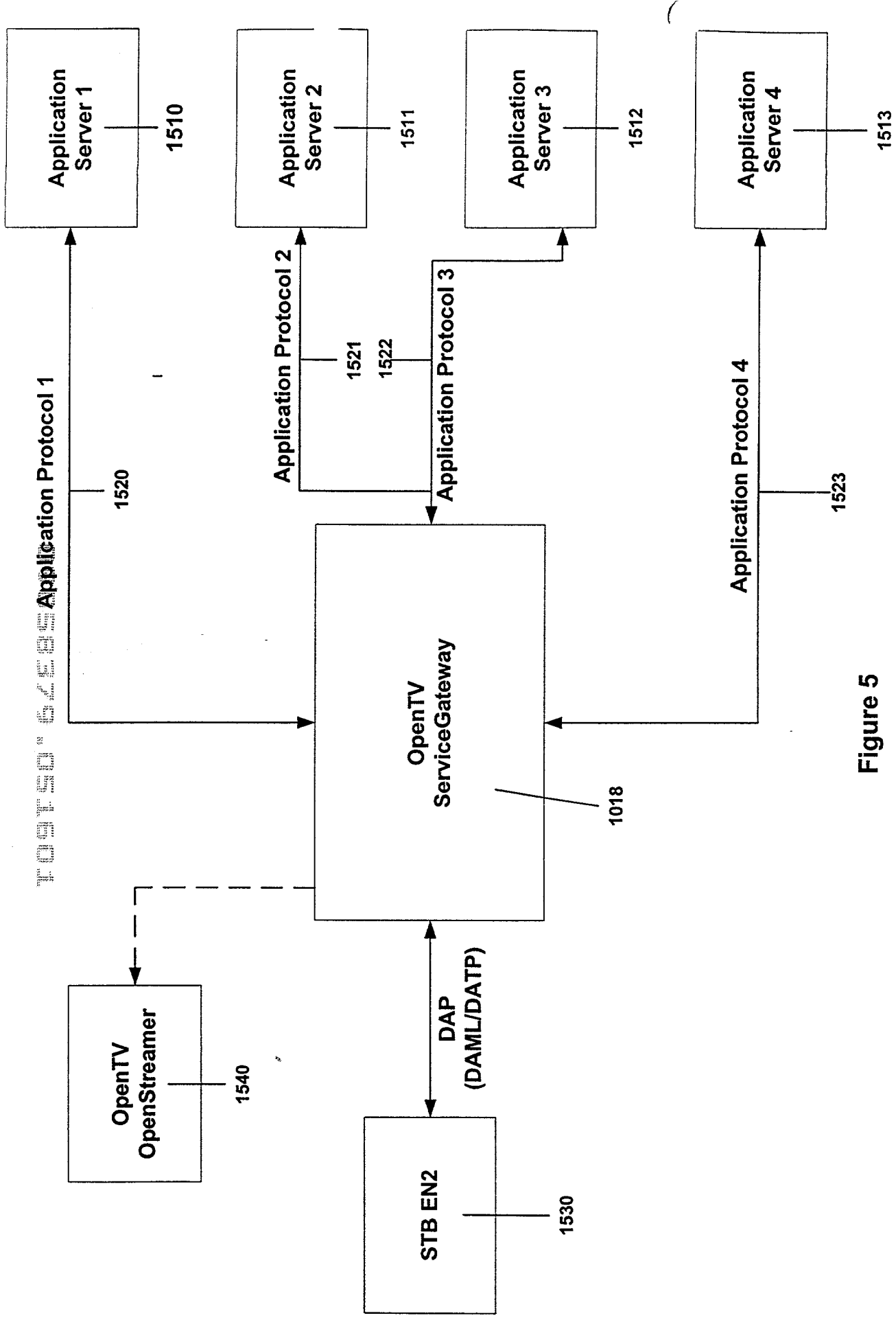


Figure 5

DAP

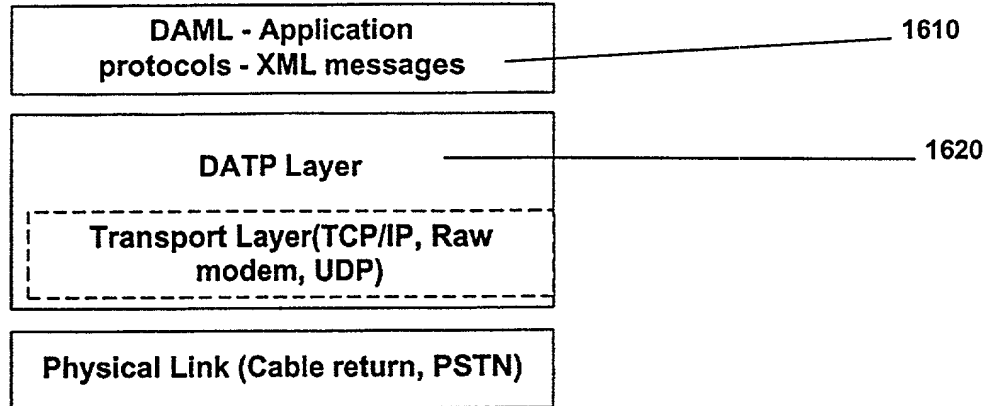


Figure 6

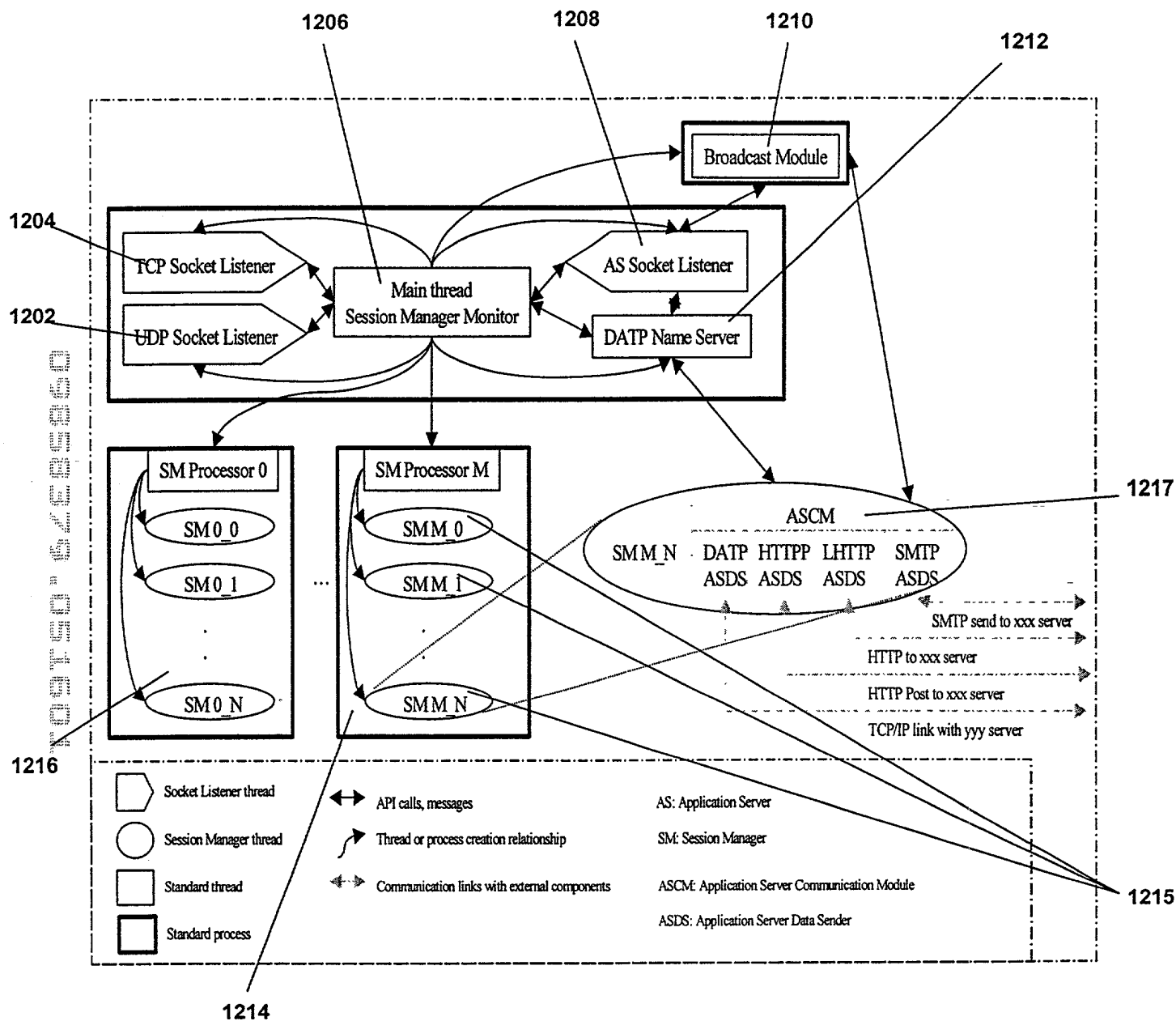


Figure 7

The diagram illustrates a memory stack structure. At the bottom, a horizontal bar is divided into six equal segments, each labeled "SEQ NUM". Below each segment is a vertical line pointing to a specific address: 830, 840, 850, 860, 870, and 880. Above the stack, two horizontal double-headed arrows represent time intervals. The first arrow, labeled t_1 , starts at the vertical line for address 820 and ends at the vertical line for address 870. The second arrow, labeled t_2 , starts at the vertical line for address 810 and ends at the vertical line for address 880. The addresses 820 and 810 are indicated by vertical lines pointing to the horizontal axis above the stack segments.

Figure 8

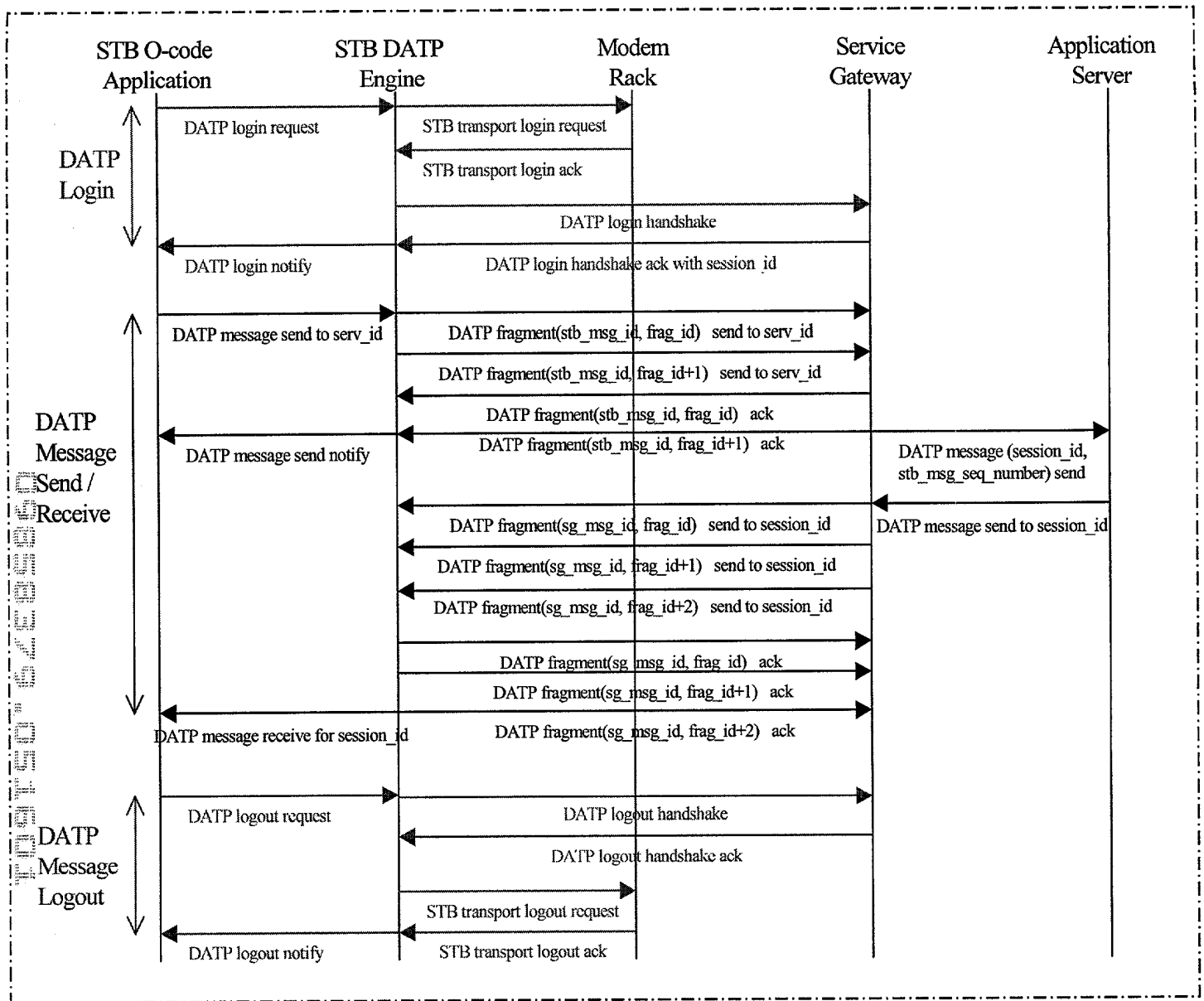


Figure 9

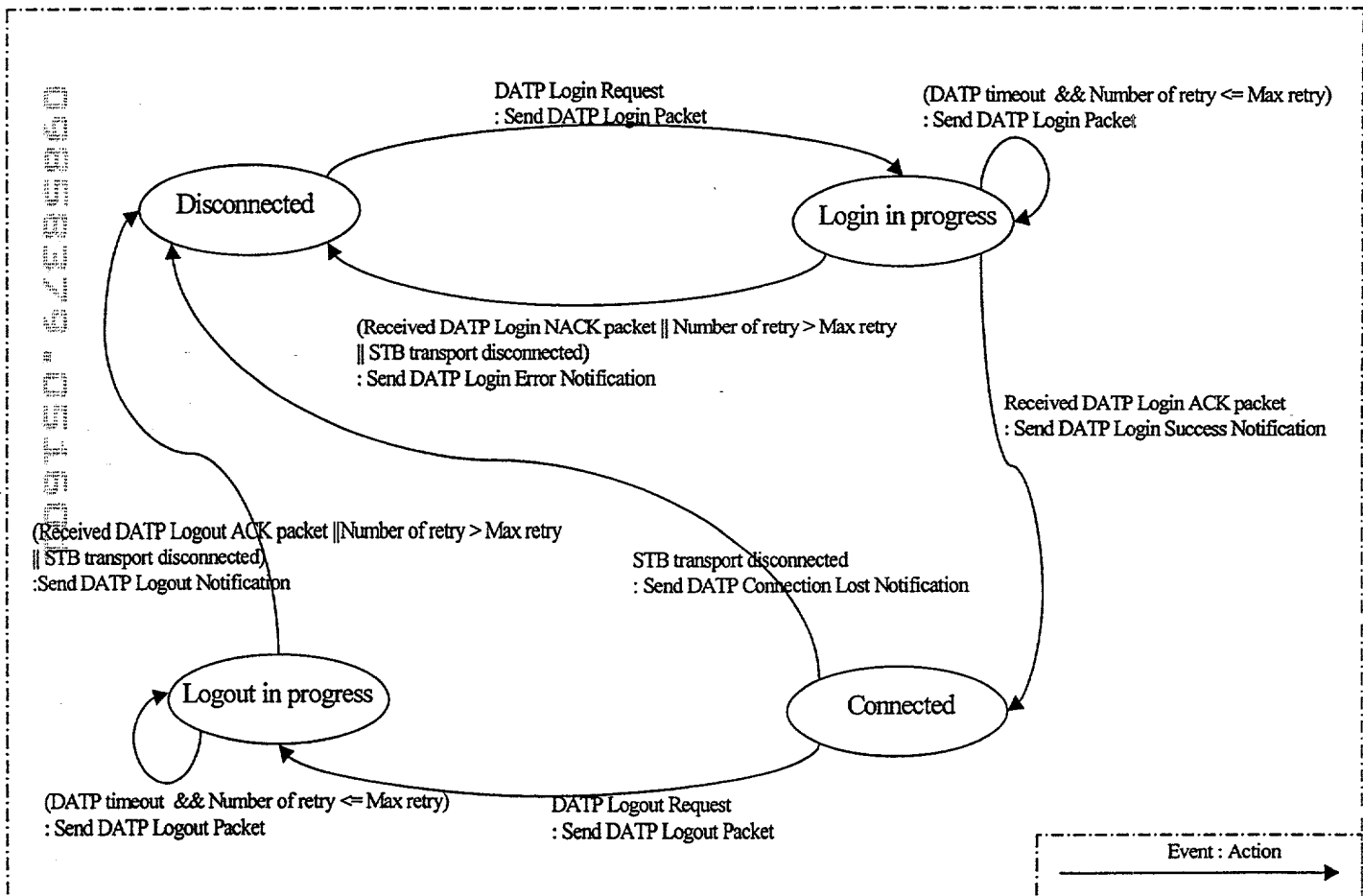


Figure 10

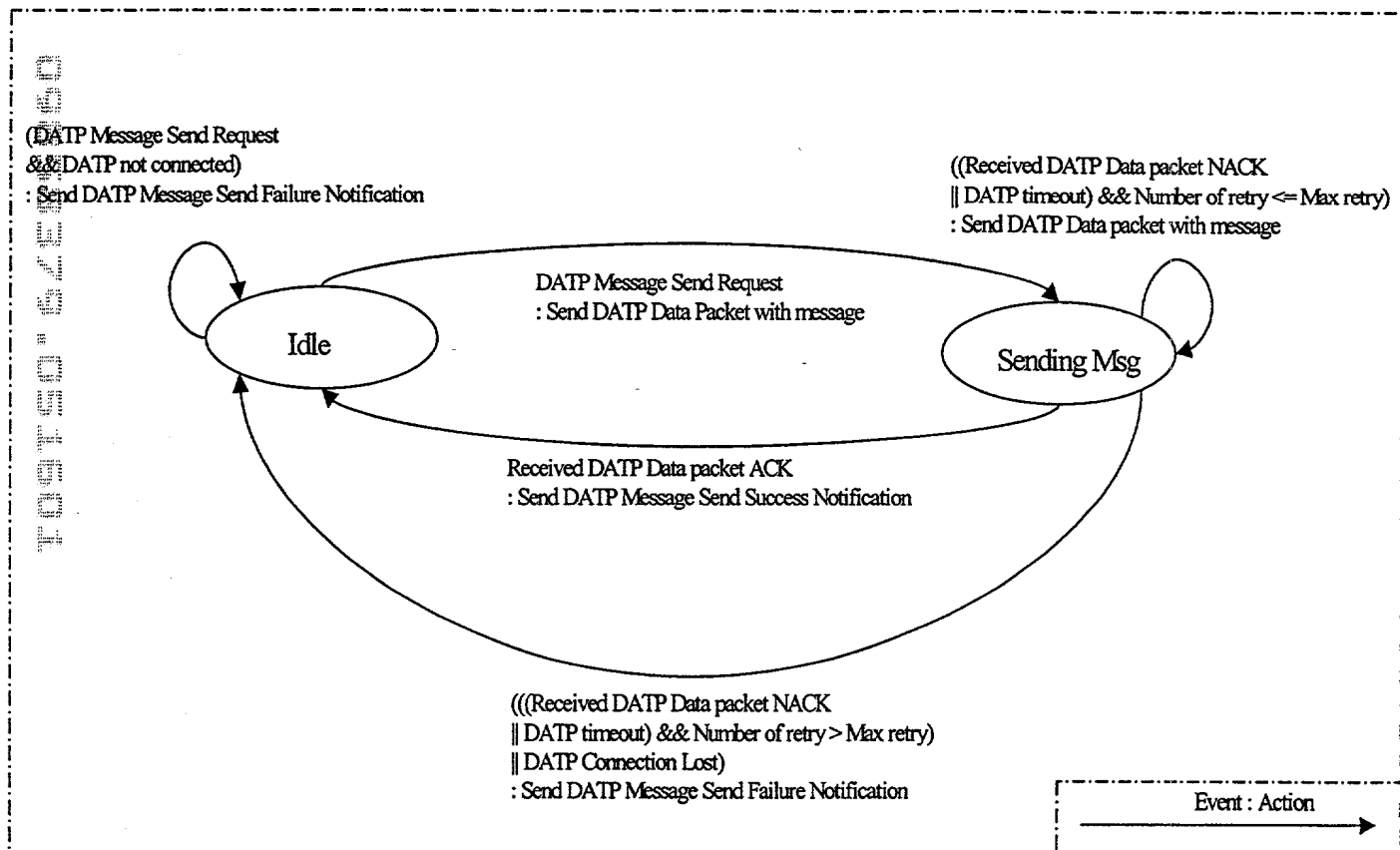


Figure 11

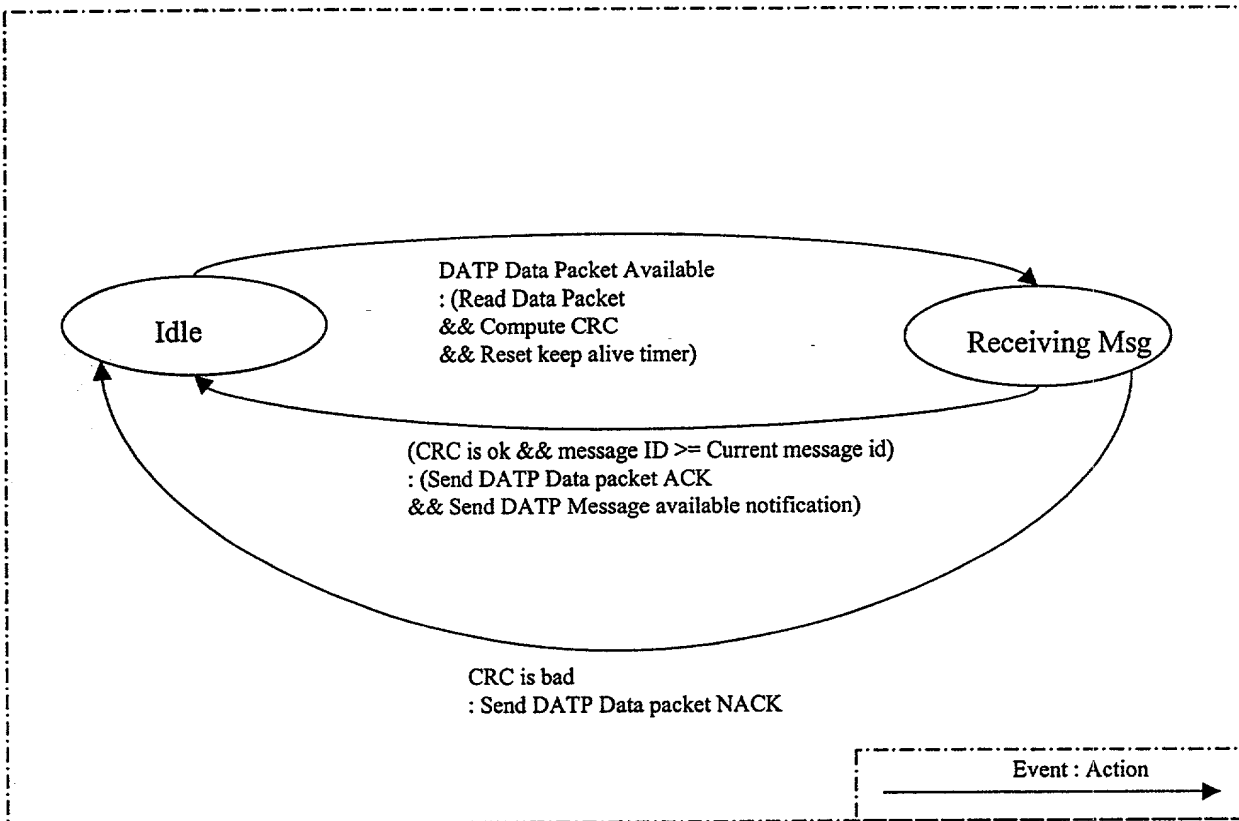


Figure 12

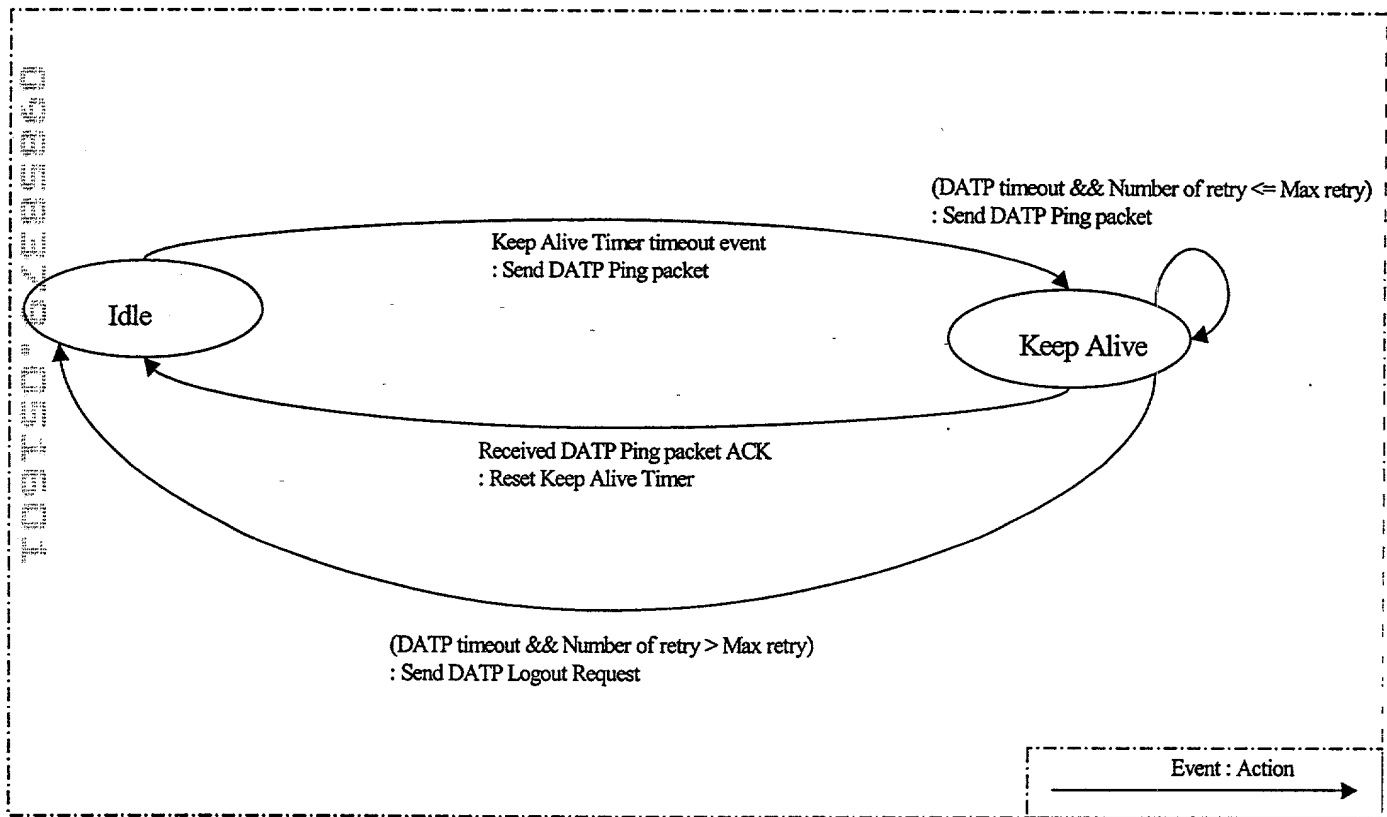


Figure 13

FIG. 14

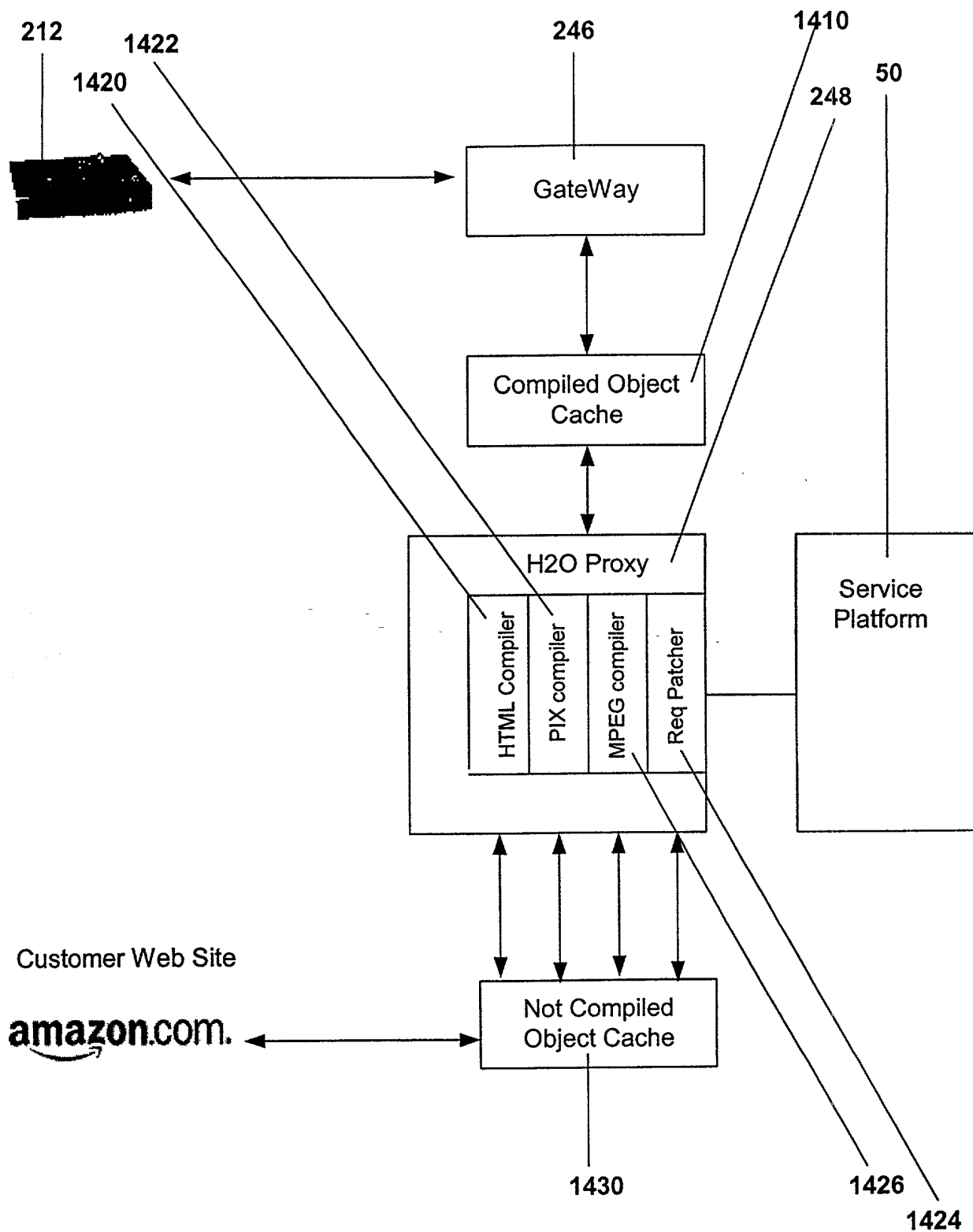


Figure 14

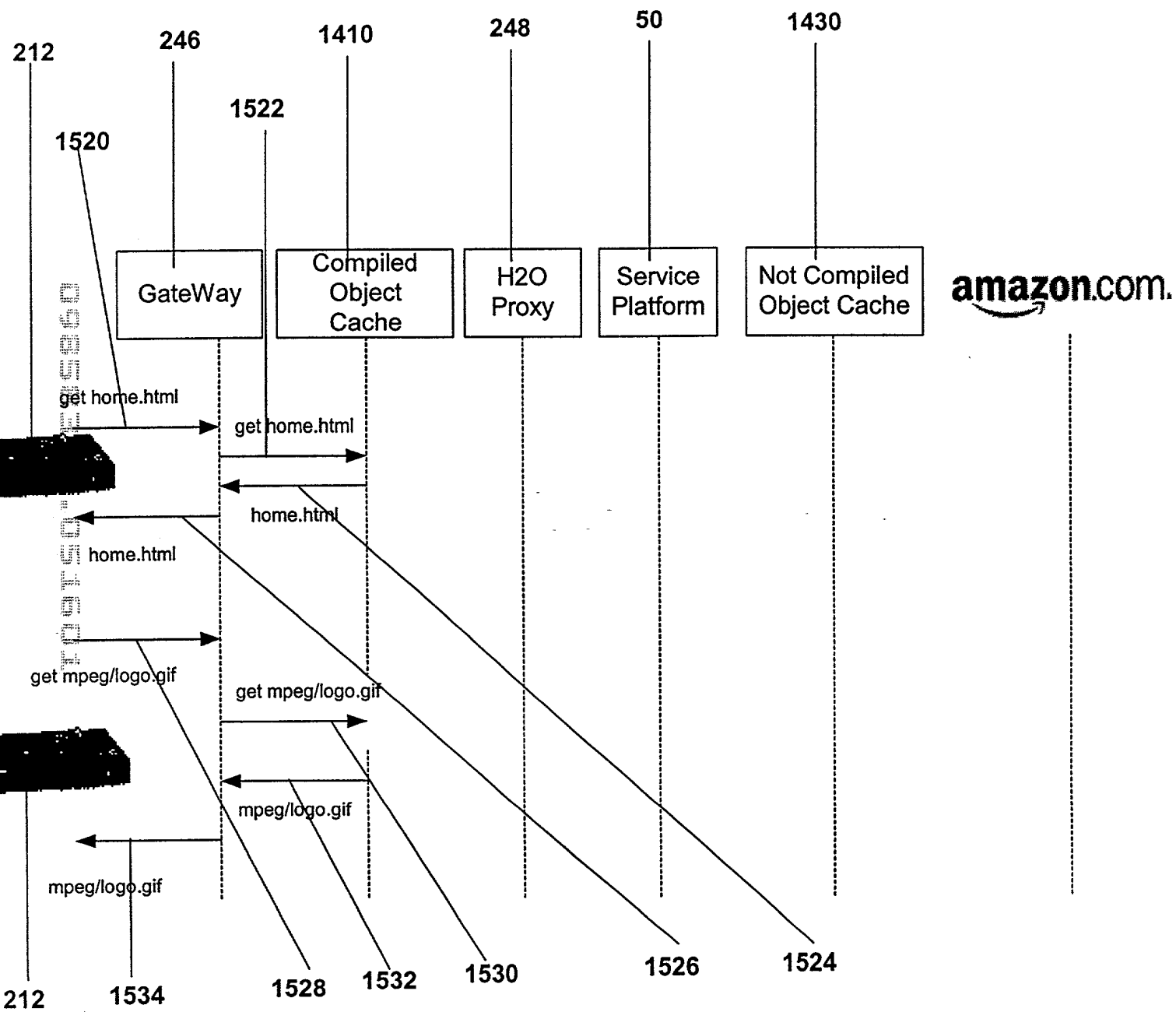


Figure 15

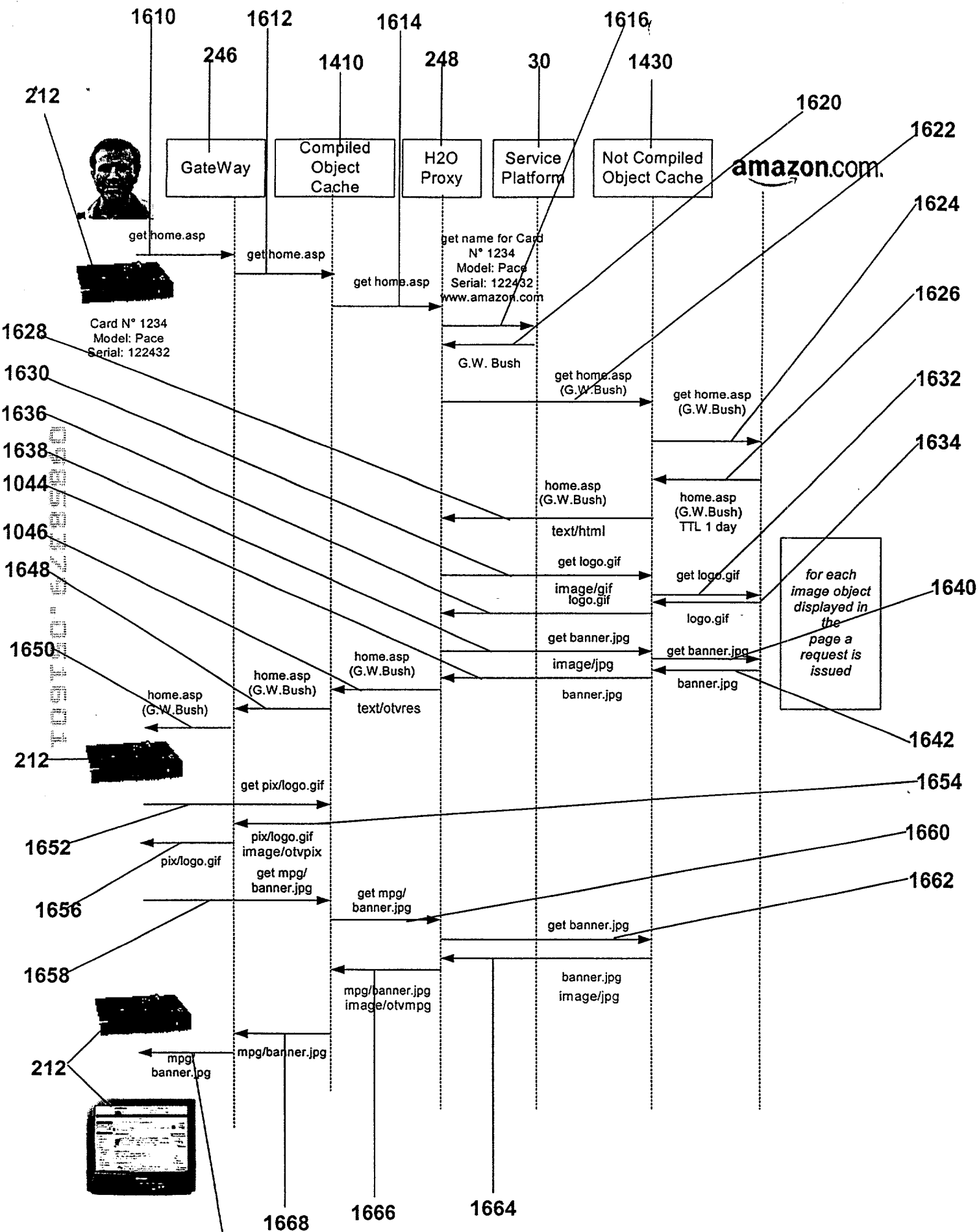


Figure 16

FIG. 17

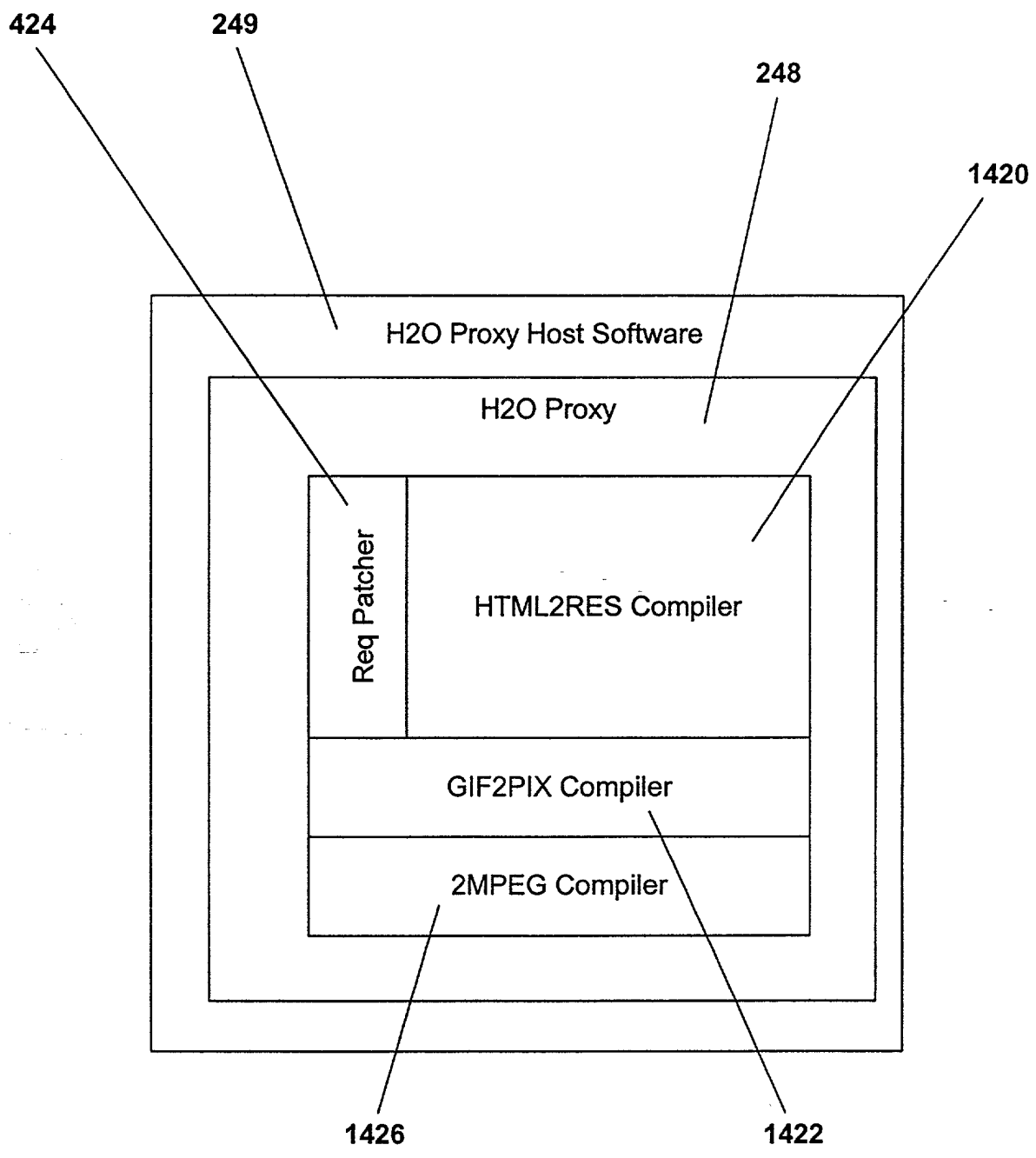


Figure 17

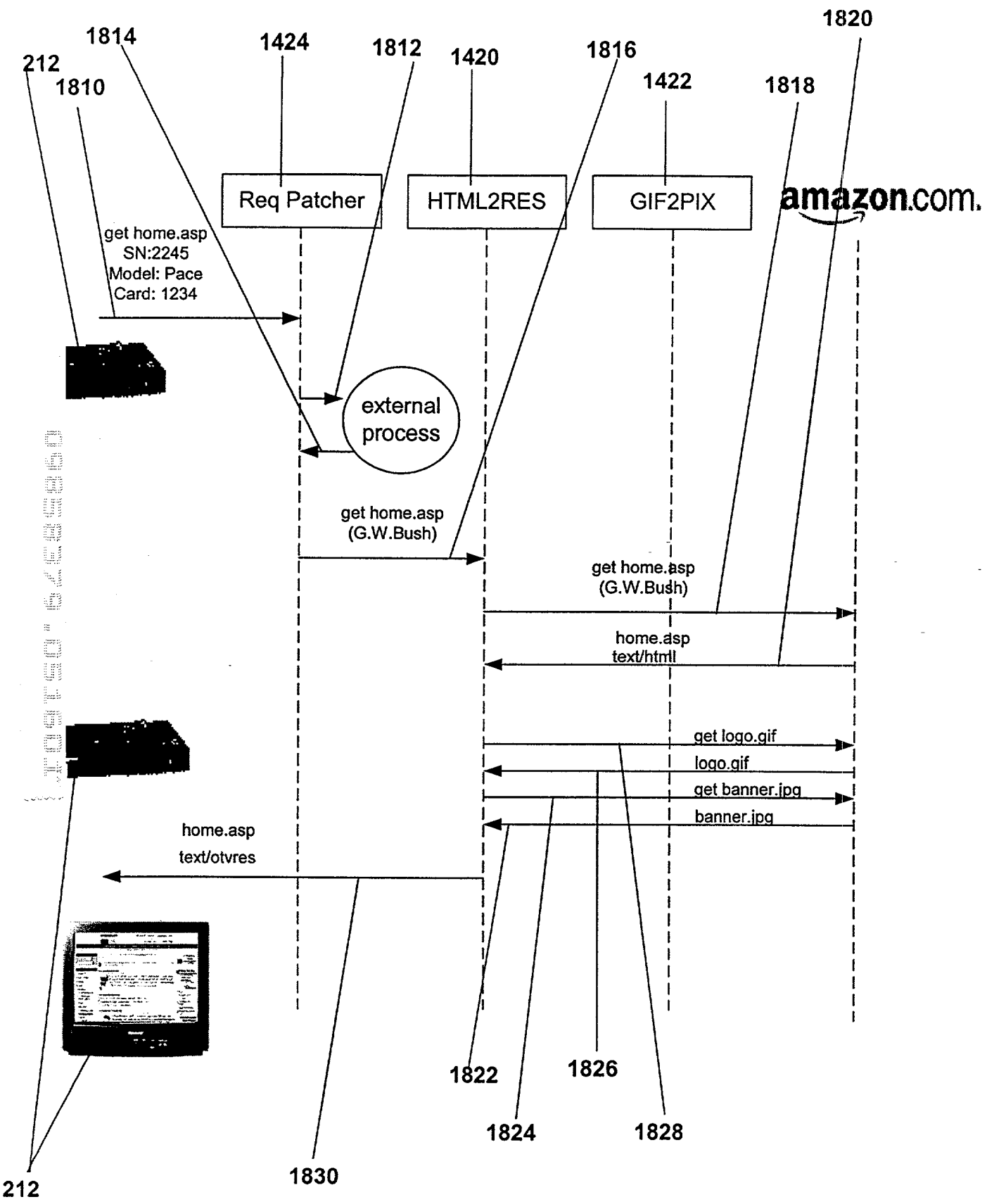


Figure 18

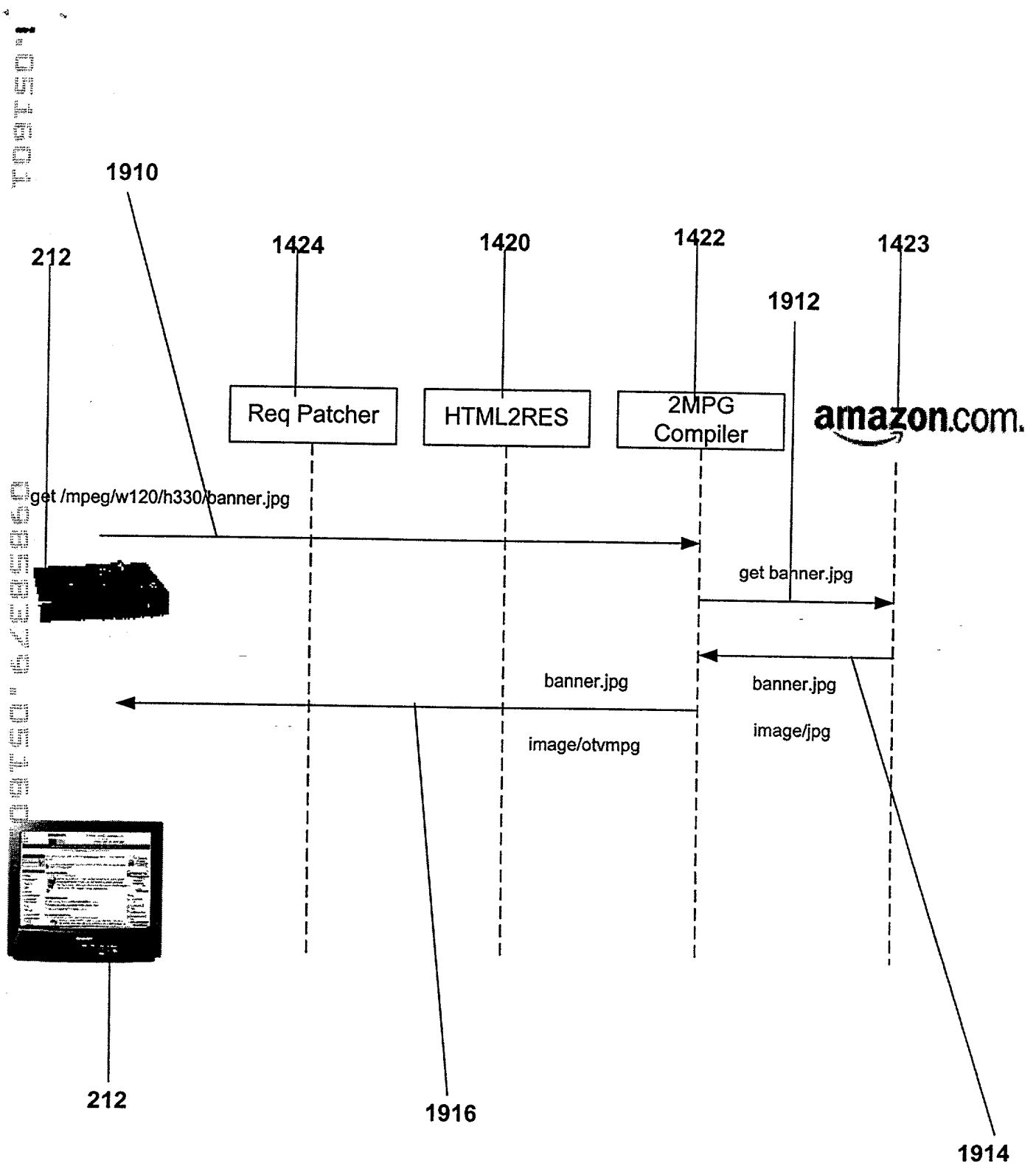


Figure 19